
Faculty Vitae

1. Name

Mustafa Abdullah Saeed

2. Education

1 - **BSc.**\Electronic and Electrical Engineering\University of Technology\Iraq\2010

2- **MSc.**\Electronic and Electrical Engineering\University of Leeds\United Kingdom\2015

3. Academic experience

2018-2019

(Lecturer)- University of Information Technology and Communication- Collage of Engineering.
Baghdad-Iraq

Electrical Circuit Analysis ,

Electrical Circuit Analysis Lab,

Communication Lab

Electronic Lab

2017-2018

(Lecturer)- University of Information Technology and Communication- Collage of Engineering.
Baghdad-Iraq

Electrical Circuits lab, Enigeering Drawing

9. Publications/Presentations

Thesis

Master: Implementation of FIR Filter Using Different FPGA Design Flows

