
Faculty Vitae

1. Name

Yasser M. Abd

2. Education

B.Sc Electrical Engineering at University of Technology / 2012

M.Sc Electrical Engineering at University of Technology /2014

3. Academic experience

College of Business informatics (UOITC) –Baghdad, lecturer, March 2016, full time.

University of Technology (UOT) Baghdad, lecturer, 2015, part time.

4. Non-Academic experience

- **Hardware**
- FPGA, Electronic devices, Solar cell, Computer and LED street lighting.
- Mobile applications.
- Chess game developer implemented on FPGA

Software

Mobile applications

C++ programmer

- Xilinx ISE
- MATLAB
- 8086 microprocessor
- Proteus

7. Honors and awards

- I've got to shield the university by the University of Technology to complete a master's pre-specified period in 2015.
 - Received Certificate of appreciation from University of Technology / department of Electrical Engineering for *third level* during B. Sc. Study (fourth year)/2011-2012
 - Received Certificate of appreciation from University of Technology / department of Electrical
-

- Other Honors and awards in different activity.

9. Publications/Presentations

Hanan A. R. Akkar , Yaser M. Abid, “Design of Intelligent Controller for Solar Tracking System Based on FPGA”, [Iraq Academic Scientific Journals](#), Vol. 1, Issue 33, 2014, PP. 114-128.

Yaser M. Abid Dr. Hussein A. Mohammed, Alaa Hamza Omran, Ahmed Raheem “Intelligent Controller for Monitoring Vehicles at the Roads”, Al-Nahrain Journal for Engineering Science.

Alaa Hamza, Yaser M. Abid, Rouqia Jwad, “Supervised feed forward neural networks for Smart chessboard based on FPGA”, journal of Engineering and Applied Science.

Alaa Hamza Omran, Yaser M. Abid, Dr. Huda Kadhim, “Design of Artificial Neural Networks System for Intelligent Chessboard”, [2017 4th IEEE International Conference on Engineering Technologies and Applied Sciences \(ICETAS\)](#), 29 Nov.-1 Dec. 2017,IEEE, Bahrain.

Alaa Hamza Omran, Yaser M Abid, Ahmed Sabah Ahmed, Huda Kadhim, Ruqaia Jwad, “Maximizing the power of solar cells by using intelligent solar tracking system based on FPGA”, Advances in Science and Engineering Technology International Conferences (ASET), IEEE,2018, Abu Dhabi, United Arab Emirates.